

ABOUT THE FACULTY DEVELOPMENT PROGRAMME:

The Electronics and Communication Engineering Department, Deenbandhu Chhotu Ram University of Science and Technology, Murthal, Sonapat is Organizing AICTE Sponsored Two Weeks Faculty Development Programme on **“Signal & Image Processing: Issues, Challenges & Techniques”** from **01st-12th January, 2018**.

The FDP is designed to bring Researchers, Academia and Technocrats from different parts of the country to a common gathering for exchanging and sharing the recent developments in the field of Signal & Image Processing and its allied areas.

The Experts from prestigious Universities & Institutes such as IIT Delhi, IIT Roorkee, DTU, Thapar University, NIT, and from DCRUST will deliver expert talks and practical demonstration in the course.

The number of participants from different Institutions/Universities and Industries are expected to participate. Due to limited number of seats as per guidelines of AICTE the registration will be done on the first cum first basis. The T.A/D.A and Boarding & Lodging facility will be provided to outstation participants from AICTE approved Institutions/Universities as per Deenbandhu Chhotu Ram University of Science and Technology, rules.

The participants are going to gain extensive details of subject knowledge along with practical knowledge. Case studies and practical examples enrich their concept in the field of Signal and Image Processing and it will help them to do better teaching and to conduct research activities at their institutes.

MAIN OBJECTIVES OF FDP:

- To provide a forum to exchange views, ideas & the latest innovations in the field of Signal & Image Processing.
- To offer learning on basics, emerging trends & challenges in the field of Signal & Image Processing
- To improve faculty's ability in carrying out research, testing & consultancy in the area of Signal & Image Processing.
- To introduce Signal & Image Processing and to provide opportunities to further widen the knowledge and to pursue research.
- To provide hands-on training using MATLAB to demonstrate fundamental concepts of Signal & Image Processing i.e Signal representation, time & frequency domain analysis, signal transformation, filter design, image acquisition, enhancement, smoothing, denoising, compression, segmentation etc.

TOPICS TO BE COVERED:

- Signal Representation in Time & Frequency Domain
- Signal Transformation
- Filter Designing
- Digital Image Processing

- Recent Trends in Image Enhancement, Image Restoration, Denoising, Compression , Edge Detection & Segmentation
- Recent Research Areas in Signal & Image Processing
- Various Issues and Techniques in Signal & Digital Image Processing
- Applications of Digital Image Processing such as Finger Print Recognition, Face Recognition, Character Recognition, Disease Diagnosing using MRI, X-Ray, Ultrasound etc.
- Soft Computing Techniques for Signal and Image Processing
- MATLAB : Signal & Image Processing Toolbox
- Any other Related Topics

How to Apply:

Candidate required to send the scanned copy of completely filled prescribed registration form at fdpsipict2018@gmail.com on or before 15th December, 2017.

(Original Registration Form duly completed has to be submitted on registration desk at the time of reporting).

For Any Other Query, Kindly Contact to:

Dr. Rajeshwar Dass (Coordinator)

Assistant Professor, ECED, Room No. 335,

MV Block, DCR University of Science and Technology, Murthal, Sonapat-131039.

Email: fdpsipict2018@gmail.com

M. No: 09812688887

Dr. Prachi Chaudhary (Co-Coordinator)

Assistant Professor, ECED, Room No. 304, MV Block, DCR University of Science and Technology, Murthal, Sonapat-131039.

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Ms. Rajni (Co-Coordinator)

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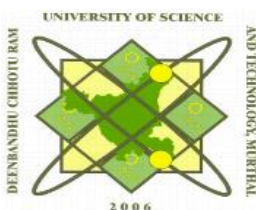
**AICTE Sponsored
Two Weeks
Faculty Development Programme
on
“Signal & Image Processing:
Issues, Challenges & Techniques”**

(SIPICT-2018), 1st - 12th January 2018



Organized By:

**Department of Electronics and
Communication Engineering**



**Deenbandhu Chhotu Ram University of
Science & Technology, Murthal,
Sonepat-131039 (Haryana)**

**DEENBANDHU CHHOTU RAM UNIVERSITY
OF SCIENCE AND TECHNOLOGY**

(A Haryana State Government University, Accredited Grade “A” by NAAC)

Deenbandhu Chhotu Ram University of Science and Technology (erstwhile C. R. State College of Engineering) was established through an Act of Haryana State Legislature on 6th November, 2006 to facilitate and promote studies and research in emerging areas of higher education with focus on new frontiers of Science, Engineering, Technology, Architecture and Management studies. The University is recognized under 12 (B) of the UGC Act, 1956. The University has been accorded ‘Affiliating Status’ by Act of Haryana State Legislature on 3rd August, 2011. The University provides an outstanding learning environment for multifaceted development of students, thus emerging as a centre of excellence. The University is spread over 273 acres and has a fully residential campus with ample built up space in a contemporary architectural idiom. It has 16 teaching departments under 6 different Faculties. In addition to research facilities leading to enrolment of number of research scholars, the University is presently running 31 Postgraduate and 9 Undergraduate programmes on regular basis. The University has highly qualified faculty, well equipped library, computer centre with high speed internet facility, faculty rooms, classrooms, & substantial sports facilities, etc. The University has received grant under TEQIP I, II & III of MHRD of Government of India and Various departments have successfully undertaken many UGC / AICTE / DST / DBT sponsored projects.

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING**

The Department of Electronics & Communication Engineering occupies a prominent place within the overall structure of the erstwhile CRSCE since 1987 & DCRUST since 2006. The Department offers Bachelor of Technology & Master of Technology Programme in Electronics & Communication Engineering and VLSI Design with an intake of 66, 36 and 18 respectively. The UG & PG programmes are accredited by NBA and awarded Grade “A” by NAAC. Twenty-five research scholars (Ph.D.) are registered in the department. The course curriculum is designed with the inputs from academia as well as industry in such a way that optimum magnitude has been assigned to basic as well as advance course to keep the students abreast of basics as well as advances in the specific areas of

technology. The department strives to keep various courses up-to-date so that courses reflect current developments. The faculty constantly endeavours & engages themselves in curriculum design. The department has well equipped and spacious laboratories with all modern facilities for experimentation at UG and PG levels. The laboratories have been modernized using liberal grants from TEQIP (I & II), Haryana State Government and other funding agencies. Besides teaching, the department is also actively engaged in research activities in various emerging areas such as Signal & Image Processing, Wireless Communication, Reliability, Optical Fibre Communication, VLSI Design, etc. The department has organised many National Conferences/ Seminars/ Workshops/ STCs/ FDPs etc. in emerging and thrust areas in Electronics & Communication Engineering. The FDP on SIPICT-2018 is another endeavour in the dissemination of knowledge.

ABOUT THE FDP

Aim:

Faculty Development Programme (FDP) on “*Signal & Image Processing: Issues, Challenges & Techniques*” SIPICT-2018 has been designed to bring Researchers, Academia and Technocrats from different parts of the country to a common gathering for exchanging and sharing the recent developments in the field of Signal & Image Processing and it’s allied areas.

Objectives:

- To provide a forum to exchange views, ideas & the latest innovations in the field of Signal & Image Processing.
- To offer learning on basics, emerging trends & challenges in the field of Signal & Image Processing
- To improve faculty’s ability in carrying out research, testing & consultancy in the area of Signal & Image Processing.
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TOPICS TO BE COVERED

- Signal Representation in Time & Frequency Domain
- Signal Transformation, Filter Designing
- Digital Image Processing, Recent Trends in Image Enhancement, Image Restoration, Denoising, Compression, Edge Detection & Segmentation, Recent Research Areas in Signal & Image Processing, Various Issues and Techniques in Signal & Digital Image Processing, Various Applications of Digital Image Processing such as Finger Print Recognition, Face Recognition, Character Recognition, Disease Diagnosing using MRI, X-Ray, Ultrasound etc.
- Soft Computing Techniques for Signal and Image Processing
- MATLAB: Signal & Image Processing Toolbox
- Any other Related Topics

ELIGIBILITY

Faculty members from academic institutions (Technical Colleges/ Universities/ Research Organizations) and professionals from industries are eligible to attend the FDP programme. Research Scholars working in the same field are also eligible to attend the FDP. Eligible outstation participants from AICTE approved institutions will be reimbursed their T.A/D.A as per University rules

INVITED SPEAKERS

Faculty from Prestigious Universities & Institutes such as IIT Delhi, IIT Roorkee, PEC, DTU, NIT Delhi, NSIT, NITTTR, will deliver expert lectures followed by practical demonstration from industry experts.

REGISTRATION FORM

(As per prescribed format attached)

Candidate required to send the scanned copy of prescribed registration form at fdpsipict2018@gmail.com on or before 20th December, 2017.

(Original Registration Form duly completed has to be submitted on registration desk at the time of reporting)

IMPORTANT DATES

Programme Duration : Two Weeks

Fee of Course : Nil

Starting of the Course : 01.01.2018.

Receipt of Registration Forms: ~~15.12.2017~~ 20.12.2017

(by hand/ post/ email)

Intimation to Candidates: 20.12. 2017.

CORE COMMITTEE

Patron

Professor(Dr.) Rajenderakumar Anayath
(Hon'ble Vice-Chancellor)

Dean Faculty of Engg.

Professor(Dr.) J.S Saini
(Chairman, EED)

Registrar

Professor(Dr.) S.K Garg

Chairperson

Professor(Dr.) Manoj Duhan
(Chairman, ECED)

Coordinator

Dr. Rajeshwar Dass
(Assistant Professor, ECED)

Co-Coordination

Dr. Prachi Chaudhary
(Assistant Professor, ECED)

Ms. Rajni

(Assistant Professor, ECED)

CONTACT PERSON(S)

Dr. Rajeshwar Dass (Coordinator)

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MV Block, DCR University of Science and
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Email: fdpsipict2018@gmail.com, **M.No:**09728680352.

ORGANIZING COMMITTEE

Dr. Manoj Duhan, Professor & Chairman

Dr. Amit Kumar Garg, Professor

Dr. S.K Grewal, Associate Professor

Dr. Poonam Singal, Associate Professor

Mr. Manish Jain, Associate Professor

Dr. Priyanka, Associate Professor

Dr. Geetanjali Pandove, Associate Professor

Dr. Pawan Dahiya, Assistant Professor

Dr. Mridul Chawla, Assistant Professor

Dr. Prachi, Assistant Professor

Dr. Rajeshwar Dass, Assistant Professor

Ms. Sunita Malik, Assistant Professor

Ms. Rekha Yadav, Assistant Professor

Ms. Himanshi Saini, Assistant Professor

Ms. Kusum Dalal, Assistant Professor

Ms. Rajni, Assistant Professor

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Mr. Satish Kumar Nakra, A.T.O

Mr. Naresh Kumar, A.T.O

Mr. Satish Yadav, Lab Technician

Mr. Pankaj, Lab Technician

Mr. Rajesh Kumar, Lab Technician

Mr. Surender Kumar, Lab Technician

Mr. Sher Singh, Lab Technician

Mr. Pardeep, Lab Technician

Mr. Viresh Dahiya, DEO



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On
Signal & Image Processing: Issues, Challenges & Techniques
(SIPICT-2018), 01st-12th January, 2018



REGISTRATION FORM

Name (in Block Letters):

Date of Birth & Age: Academic Qualification:

Designation: Specialization:

Department /Institution:

Teaching Experience:

Correspondence/ Permanent Address:

Telephone Numbe: Landline..... Mobile.....

Email ID:

Accommodation Required: (Yes/No)

Is Your Institute approved by AICTE?: (Yes/No)

No. of Summer/Winter Schools/STTPs Attended:

DECLARATION BY CANDIDATE:

The given information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the course for the entire duration.

Place:

Date:

Signature of participant

SPONSORSHIP:

Mr./Ms./Dr. is an employee of our institution and is hereby sponsored. He/She will be permitted to attend the programme, if selected.

Date:

Sign. and Seal of Head of Institution